

ATTORNEY DOCKET N°: 03-0805  
EXPRESS MAIL LABEL N°: EV 338 284 150 US

PATENT



**PATENT APPLICATION TRANSMITTAL LETTER**

MAIL STOP PATENT APPLICATION  
COMMISSIONER FOR PATENTS  
P.O. BOX 1450  
ALEXANDRIA, VA 22313-1450

Transmitted herewith for filing is the patent application of:

**INVENTORS:**

Sean C. Erickson	Kevin R. Nunn	Norman E. Mause
2918 Sagebrush Drive	729 Parkview Drive	1733 Willow Springs Way
Fort Collins, CO 80525	Fort Collins, CO 80525	Fort Collins, CO 80528
Citizen of United States	Citizen of United States	Citizen of United States

**Title: Low Gate Resistance Layout Procedure for RF Transistor Devices**

---

**CERTIFICATION UNDER 37 C.F.R. §1.10**

I hereby certify that this is Application Transmittal and the documents referred to as enclosed therein are being deposited with the United States Postal Service on this date, July 25, 2003 in an envelope as "Express Mail Post Office to Addressee", Mailing Label N° EV 338 284 150 US, with sufficient postage, addressed to: MS Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

  
ReNea D. Berggren

DATED: July 25, 2003

**1. Type of Application**

This is application.

**2. Benefit of Prior U.S. Application(s) (35 U.S.C. § )**

USSN

Filing Date

Inventor(s)

Status

**3. Papers Enclosed That Are Required for Filing Date under 37 C.F.R. §1.53(b) (Regular) or 37 C.F.R. §1.153 (Design) Application**

Ten (10) Pages of Specification;  
Five (5) Pages of Claims;  
One (1) Page of Abstract; and  
Five (5) Sheets of Drawing Figures.

**4. Additional Papers Enclosed**

Information Disclosure Statement

**5. Declaration or Oath**

The signed Declaration and Power of Attorney is attached

**6. Inventorship Statement**

The inventorship for all the claims in this application are the same.

**7. Language**

English.

**8. Assignment**

The signed Assignment is attached. The Recordation Form Cover Sheet is enclosed.

**9. Certified Copy**

None are required.

**10. Fee Calculation (37 C.F.R. §1.16)**

BASIC FEE									\$750.00
-----------	--	--	--	--	--	--	--	--	----------

EXCESS CLAIM FEE									
------------------	--	--	--	--	--	--	--	--	--

TOTAL OVER TWENTY	27	-20	=	7	X \$18.00		\$126.00
-------------------	----	-----	---	---	-----------	--	----------

INDEPENDENT OVER THREE	3	-3	=	0	X \$84.00		\$0.00
------------------------	---	----	---	---	-----------	--	--------

MULTIPLE DEPENDENT				0	X \$280.00		\$0.00
--------------------	--	--	--	---	------------	--	--------

ASSIGNMENT RECORDATION FEE					\$ 40.00		\$40.00
----------------------------	--	--	--	--	----------	--	---------

TOTAL FILING FEES							<u><u>\$916.00</u></u>
-------------------	--	--	--	--	--	--	------------------------

**11. Small Entity Statement(s)**

None required.

**12. Request for International - Type Search (37 C.F.R. §1.104(d))**

None required.

**13. Authorization to Charge Fees**

The Commissioner is hereby authorized to charge the filing fees of **\$916.00** to Deposit Account N° 12-2252. Please charge any underpayments related to this filing or credit any excess to Deposit Account N° 12-2252. A copy of this *Transmittal* is enclosed for accounting purposes only.

Please direct all correspondence and telephone calls to:

PETER SCOTT  
INTELLECTUAL PROPERTY LAW DEPARTMENT  
LSI LOGIC CORPORATION  
M/S D-106  
1551 MCCARTHY BLVD.  
MILPITAS, CA 95035



DATED: July 25, 2003.

Respectfully submitted,  
Sean C. Erickson et al.,  
LSI Logic Corporation,

By Walter J. Malinowski  
Walter J. Malinowski  
Reg. N° 43,423

SUITER • WEST PC LLO  
14301 FNB PARKWAY, SUITE 220  
OMAHA, NE 68154-5299  
(402) 496-0300 (TELEPHONE)  
(402) 496-0333 (TELECOPIER)

**This Transmittal Ends With This Page.**